Attorney's Docket No. 9180-12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Curtis Grant Jones et al;

Serial No.:

For:

not assigned yet

concurrently herewith 1-11:00

Filed:

ELECTROPLATING METHODS INCLUDING MAINTAINING A DETERMINED

ELECTROPLATING VOLTAGE AND RELATED SYSTEMS

Date: January 11, 2002

Box Patent Application Commissioner for Patents Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted

Registration No. 38,176

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PATENT TRADEMARK OFFICE

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"Express Mail" mailing label number EL 733099245 US Date of Deposit: January 11, 2002

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Box Patent Application, Commissioner of Patents, Washington, DC 20231.

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STATEMENT BY APPLICANT				First Named Inventor	Curtis Grant Jones	0	
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(use as many sheets as necessary)				Examiner Name	not yet assigned	=	0
Sheet	1	of	1	Attorney Docket Number	9180-12	<u>_</u>	

	-		Ü.	S. PATENT DOCUMENTS			
Examiner Initials*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
IIIIIais		Number	Kind Code (if known)	of Cited Document	MM-DD-YYYY	or Relevant Figures Appear	
	1.	5,293,006		Yung	03/08/1994		
	2.	6,117,299		Rinne et al;	09/12/2000		
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FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant	T	
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		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	1			
Examiner Initials*	Cite No.	agrical symposium potalog etc.) data paga(a) valuma issua numbar(a) publishar city and/or country whore published				
	3. Solomon "Providing High Density and Performance for Chip-to System Interconnection" Advanced Packaging (November 2001) pp 19-28					
	4.	Yung et al; "Flip-Chip Process Utilizing Electroplated Solder Joints"; Proceedings of the Technical Conference, (Sep. 10-12, 1990) International Electronics Packaging Conference Malborough, Massachusetts pp1065-1073				
Examiner S	ignatur	Date Considered				

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.